# **Dual Matched MMIC Amplifier**

PHA-11+

.05 to 3 GHz  $50\Omega$ 



# The Big Deal

- Dual matched amplifier for push-pull & balanced amplifiers
- High IP2 and IP3
- May be used as a replacement to WJ AH11<sup>a,b</sup>

# CASE STYLE: DL1020

# **Product Overview**

Mini-Circuits PHA-11+ is a dual matched wideband amplifier fabricated using advanced E-PHEMT technology, offering high dynamic range (High IP3 and Low NF) for use in 50 and 75 ohm applications. Exceptionally high IP2 has been demonstrated in wideband 50 and 75 ohm amplifiers evaluation boards. Combining this with low noise figure to enable it for use in exceptionally high dynamic range amplifiers.

# **Key Features**

Feature	Advantages
Broadband	Covers Cable TV band and communication bands such as Cellular, Cable TV, PCS, WiMAX etc.
Matched pair for use in exceptionally high IP3 and IP2 amplifiers	Typical gain match of 0.2 dB and phase match of 1.6 deg. enables it to be used in push-pull amplifiers. Outstanding IP2.
High IP3, up to 43 dBm	Ideal for suppressing unwanted intermods in the presence of multi carriers, which is common in present day communication systems.
Low Noise Figure: 1.8 dB typical	Compare this to competitors, which in the range of 4-6 dB. Mini-Circuits amplifier improves the dynamic range.
High P1dB: 22 dBm	High P1dB enables the amplifier to operate in linear region in the presence of strong interfering signals.

a. Suitability for model replacement within a particular system must be determined by and is solely the responsibility of the customer based on, among other things, electrical performance criteria, stimulus conditions, application, compatibility with other components and environmental conditions and stresses

b. The WJ part number is used for identification and comparison purposes only.

Notes
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# **Dual Matched MMIC Amplifier**

.05-3GHz

### **Product Features**

- Two matched amplifiers in one package
- High IP3, +44 dBm at 0.8 GHz in push-pull configuration
- High IP2, +78 dBm at 0.8 GHz in push-pull configuration
- Gain, 16 dB typ. at 0.8 GHz
- P1dB, +22 dBm typ. at 0.8 GHz
- Low noise figure, 1.8 dB typ. at 0.8 GHz
- May be used as replacement for WJ AH11<sup>a,b</sup>

# **Typical Applications**

- CATV
- FTTH
- Optical networks
- Base station infrastructure
- Balanced amplifiers
- 75 Ohm push-pull and balanced amplifiers

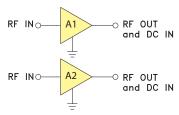


+RoHS Compliant The +Suffix identifies RoHS Compliance. See our web site for RoHS Compliance methodologies and qualifications

# **General Description**

PHA-11+ is a dual matched wideband high dynamic range amplifier. Enclosed in a 6.0 x 4.9 mm MCLP plastic package. PHA-11+ is fabricated using E-PHEMT\* technology and is ideal for use in balanced and push-pull amplifiers.

# simplified schematic (each of A1, A2) and pin description



RF OUT & DC IN GND	RF OUT GND & DC IN
8 7	6 5
A1	A2
RF IN GND 1 2	GND RFIN
1 2	3 4

Function	Pin Number	Description
RF IN, A1	1	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation. (see Application circuit, Fig 2.)
RF-OUT and DC-IN, A1	8	RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit", Fig 2
RF IN, A2	4	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation. (see Application circuit, Fig 2.)
RF-OUT and DC-IN, A2 5		RF output and bias pin. DC voltage is present on this pin; therefore a DC blocking capacitor is necessary for proper operation. An RF choke is needed to feed DC bias without loss of RF signal due to the bias connection, as shown in "Recommended Application Circuit", Fig 2
GND 2,3,6,7 & paddle		Connections to ground. Use via holes as shown in "Suggested Layout for PCB Design" to reduce ground path inductance for best performance.

<sup>\*</sup> Enhancement mode pseudomorphic High Electron Mobility Transistor.

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# Electrical Specifications $^1$ at 25 $^{\circ}$ C, Zo=50 $\Omega$ and Device Voltage 5V, unless noted

Specifications (other than Ma		Condition (GHz)	Min.	Тур.	Max.	Units
Frequency Range		Containon (Ciriz)	0.05	Typ.	3.0	GHz
requericy narige	0.05	0.03	17.7	3.0	GHZ	
	0.25	_	16.4	_		
Gain		0.45 0.8	14.5	16.3 16.1	17.7	dB
		2.0	14.5	14.5	i	
			_		_	
		3.0		12.0	_	
		0.05		11.3		
Input Return Loss		0.25 0.45		19.5 19.4		
		0.45		17.0		dB
		2.0		8.4		
	3.0		4.5			
		0.05		14.1		
		0.25		22.1		
		0.45		21.7		
Output Return Loss		0.8		18.8		dB
		2.0		10.0		
		3.0		6.0		
		0.05		22.4		
		0.25		22.7		
(0)		0.45		22.7		
Output Power @1 dB compression (2)		0.8		22.5		dBm
		2.0		21.5		
		3.0		20.4		
		0.05	_	41.4		
		0.25	_	41.4		
4-1		0.45	_	41.6		
Output IP3 (6)		0.8	37.0	41.5		dBm
	2.0	— —	42.9			
		3.0		41.8		
		0.05		1.8		
		0.25		1.8		dB
		0.45		1.8		
Noise Figure		0.8		1.8		
		2.0		2.2		
		3.0		2.7		
	I	0.05		0.11	_	
		0.25		0.12	_	
	Amplitude Unbalance	0.45		0.12		
						dB
		0.8		0.12	0.6	
		2.0		0.22	_	
Matching between A1, A2		3.0		0.25		
		0.05		0.3	_	
		0.25		0.31	_	
		0.45		0.51	_	deg.
	Phase Unbalance					
	Phase Unbalance	0.8		1.0	5.0	
	Phase Unbalance			1.6	5.0	
	Phase Unbalance	0.8		l .	i	
evice Operating Voltage	Phase Unbalance	0.8 2.0	4.8	1.6	_	V
evice Operating Current (each amplif	ier)	0.8 2.0	4.8 110	1.6 2.0 5.0 146		mA
Device Operating Voltage Device Operating Current (each amplif Device Current Variation vs. Temperat Device Current Variation vs Voltage	ier)	0.8 2.0		1.6 2.0 5.0	— — 5.2	

Absolute Maximum Ratings (4)

tocciate maximum riatinge					
Parameter	Ratings				
Operating Temperature <sup>(5)</sup>	-40°C to 85°C				
Storage Temperature	-55°C to 150°C				
Operating Current at 5V <sup>(6)</sup>	200 mA				
Power Dissipation <sup>(6)</sup>	1000 mW				
Input Power (CW)	+24 dBm				
DC Voltage (pads 5,8)	6V				

DC Voltage (pads 5,8) ov

Notes:

(1) Measured on Mini-Circuits Test Board TB-561-11+ (characterization test circuit, Fig 1a.)

(2) Current increases at P1dB

(3) Measured on evaluation boards (push-pull amplifiers)
 TB-566-50-11+, TB-666-50-11+ (50Ω) and TB-566-75-11+ (75Ω).

See Characterization Test Circuit (Fig. 1b)

(4) Permanent damage may occur if any of these limits are exceeded.
 These ratings are not intended for continuous normal operation.

(5) Defined with reference to ground pad temperature.

(6) Per single ended amplifier

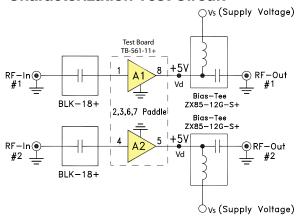
(7) Ojc= Junction Temperature-85°C
 Voltage X sum of current in A1 & A2

## Push-Pull Amplifier Typical Performance (3)

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Freq. GHZ	TB-566-75-11+ (75Ω)			TB-566-50-11+ ( 50Ω)			TB-666-50-11+ ( 50Ω)		
	Gain (dB)	Output IP3 (dBm)	Output IP2 (dBm)	Gain (dB)	Output IP3 (dBm)	Output IP2 (dBm)	Gain (dB)	Output IP3 (dBm)	OutputIP2 (dBm)
0.05	14.2	45.0	79.0	15.2	45.0	82.0	14.1	40.1	71.4
0.25	13.7	43.0	79.0	13.8	45.0	84.0	13.8	40.7	70.5
0.45	14.0	42.0	81.0	13.8	44.0	81.0	14.1	42.2	75.6
0.85	14.1	43.0	72.0	13.0	44.0	76.0	13.1	40.4	71.5
1.20	13.8	40.6	78.0	12.0	43.0	72.0	12.9	39.4	62.1
1.30	13.5	40.3	78.0				12.8	40.0	56.8
1.50							12.2	39.7	60.8
2.00							11.8	41.0	65.2
3.00							8.6	36.2	70.8

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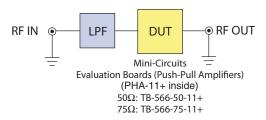
### **Characterization Test Circuit**



**Fig 1a.** Block Diagram of Test Circuit used for characterization. (DUT tested in Mini-Circuits Test board TB-561-11+, except for IP2) Gain, Return loss, Output power at 1dB compression (P1 dB), output IP3 (OIP3) and noise figure measured using Agilent's N5242A PNA-X microwave network analyzer.

### Conditions:

- 1. Gain and Return loss: Pin= -25dBm
- 2. Output IP3 (OIP3): Two tones, spaced 1MHz apart, 5 dBm/tone at output.



**Fig 1b.** Block Diagram of Test Set up used for characterization of Gain, IP2, IP3. Measured using Agilent's signal generators E8527D and Spectrum analyzer N9020A.

### Conditions:

TB-666-50-11+

1. Two tones, spaced 1MHz apart, 5 dBm/tone at output. IP2 is measured at the sum frequency of the tones.

# **Recommended Application Circuit**

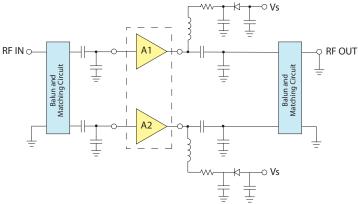


Fig 2. Recommended Application Circuit.

Refer to following Mini-Circuits Evaluation Boards for parts list.

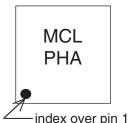
50Ω: TB-566-50-11+ 75Ω: TB-566-75-11+

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# **Product Marking**



Marking may contain other features or characters for internal lot control

Additional Detailed Technical Information additional information is available on our dash board. To access this information click here				
	Data Table			
Performance Data	Swept Graphs			
	S-Parameter (S2P Files) Data Set (.zip file)			
Case Style	DL1020 Plastic package, exposed paddle lead finish: tin/silver/nickel			
Tape & Reel	F68			
Standard quantities available on reel	7" reels with 20, 50, 100, 200, 500 or 1K devices 13" reels with 2K, 3K, 4K devices			
Suggested Layout for PCB Design	PL-322			
Evaluation Board	TB-566-50-11+ (50 $\Omega$ , 0.05-1.2 GHz) TB-566-75-11+ (75 $\Omega$ , 0.05-1.3 GHz) TB-666-50-11+ (50 $\Omega$ , 0.05-3 GHz)			
Environmental Ratings	ENV08T2			

# **ESD Rating**

Human Body Model (HBM): Class 1A (250 to <500V) in accordance with ANSI/ESD STM 5.1 - 2001 Machine Model (MM): Class M1 (<100V) in accordance with ANSI/ESD STM5.2-1999; passes 25V

## MSL Rating

Moisture Sensitivity: MSL1 in accordance with IPC/JEDEC J-STD-020D

**MSL Test Flow Chart** 

### Start Visual **Electrical Test** SAM Analysis Inspection Soak 85°C/85RH Reflow 3 cycles, Bake at 125°C, 168 hours Visual **Electrical Test** SAM Analysis Inspection

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